

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application. Please cancel claim 5 without prejudice and amend claims 1, 6, 11 and 13 as follows:

LISTING OF CLAIMS:

1. (Currently Amended) A fingerprint verification system, comprising:
 - a smart card reader including
 - a fingerprint sensor, and
 - a first microprocessor generating by minutia extraction a measured template having a plurality of measured data chunks from data read by said fingerprint sensor, each measured data chunk representing a fingerprint minutia;
 - a smart card including
 - a static memory storing a reference template having a plurality of reference data chunks,
 - a second microprocessor executing a matching algorithm for determining whether said measured template matches said reference template, and
 - a random access memory (RAM) storing a subset of said reference data chunks and a subset of said measured data chunks during execution of said matching algorithm; and
 - a communication channel between said smart card and said smart card reader,

wherein each of said measured data chunks and each of said
reference data chunks comprise:

a location of a minutia;

a minutia angle of said minutia; and

a neighborhood of said minutia.

2. (Original) The system of Claim 1, wherein said subset of said reference data chunks constitutes a substantially reduced version of said reference template.

3. (Original) The system of Claim 1, wherein said subset of said measured data chunks constitutes a substantially reduced version of said measured template.

4. (Original) The system of Claim 1, wherein a measured data chunk is loaded into said RAM of said smart card through said Communication channel.

5. (Canceled)

6. (Currently Amended) The system of Claim [[5]] 1, wherein said location comprises: a first coordinate; and a second coordinate.

7. (Original) The system of Claim 6, wherein said first coordinate is quantized.

8. (Original) The system of Claim 7, wherein said first coordinate is quantized to equal to or less than eight bits.

9. (Original) The system of Claim 6, wherein said second coordinate is quantized.

10. (Original) The system of Claim 9, wherein said second coordinate is quantized to equal to or less than eight bits.

11. (Currently Amended) The system of Claim ~~[[5]]~~ 1, wherein said minutia angle is quantized.

12. (Original) The system of Claim 11, wherein said minutia angle is quantized to equal to or less than eight bits.

13. (Currently Amended) The system of Claim ~~[[5]]~~ 1, wherein said neighborhood comprises positional parameters of a plurality of a predetermined number of neighbors.

14. (Original) The system of Claim 13, wherein said positional parameters comprises:

a distance between said minutia and a neighbor minutia;

a first angle between a first coordinate in a direction tangential to a ridge where said minutia is extracted and a line drawn between said minutia and said neighbor minutia; and

a second angle between said first coordinate and a second coordinate in a direction tangential to a ridge where said neighbor minutia is extracted.

15. (Original) The system of Claim 14, wherein said distance is quantized.

16. (Original) The system of Claim 15, wherein said distance is quantized to equal to or less than eight bits.

17. (Original) The system of Claim 14, wherein said first angle is quantized.

18. (Original) The system of Claim 17, wherein said first angle is quantized to equal to or less than eight bits.

19. (Original) The system of Claim 14, wherein said second angle is quantized.

20. (Original) The system of Claim 19, wherein said second angle is quantized to equal to or less than eight bits.

21. (Original) The system of Claim 13, wherein said neighbors are sorted.

22. (Original) The system of Claim 1, wherein said second microprocessor is an 8-bit microprocessor.

23. (Original) The system of Claim 1, wherein said second microprocessor has a speed between approximately 1 megahertz and approximately 10 megahertz.

24. (Original) The system of Claim 1, wherein said RAM is approximately 256 kilobyte in size.

25. (Original) The system of Claim 1, wherein said reference data chunks are sorted.

26. (Original) The system of Claim 1, wherein said measured data chunks are sorted.

27. (Original) The system of Claim 1, wherein said RAM stores only one reference data chunk and only one measured data chunk during execution of said matching algorithm.